

PTO/SB/08A (10-01)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/690,339
Filing Date	October 21, 2003
First Named Inventor	Derderian et al.
Group Art Unit	2818
Examiner Name	D. Le
Attorney Docket Number	2269-5713US (02-1473.00/US)

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
Dle		US-6,235,387	05/2001	Bennett et al.	—
		US- 2003/0096507	05/2003	Baker et al.	—
		US- 2004/0110010	06/2004	Buchwalter et al.	—
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FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
Dle		JP 359104523	05/1984	Imai	—	X

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Signature

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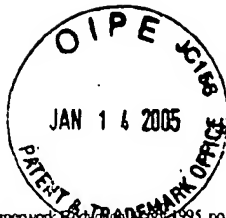
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Sheet 2 of 2

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Group Art Unit	2818
Examiner Name	D. Le
Attorney Docket Number	2269-57131US (02-1473 00/1US)

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
Dle		WOLF et al., "Silicon Processing for the VLSI Era," Vol. I: Process Technology, Lattice Press, 1986, pp. 238-9.	

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